

**QIS INSTITUTE OF TECHNOLOGY: ONGOLE**  
 (Approved by AICTE, New Delhi & Affiliated to JNTU, Kakinada)  
 (ISO 9001:2015 Certified Institution)

**DEPARTMENT OF ECE**  
**WORKSHOPS CONDUCTED**

**CAY(2018-2019)**

S. No.	Gap	Date-month-Year	ResourcePerson with Designation	NO of Students
1	Technical workshop (workshop on P SPICE)	15-12-2018	Mr. Sameulla Khan, Associate Professor Electronics and Communication Engineering, VIT-AP..	92
2	Internet of things with emphasis on sensors and interfacing	07-02-2019 TO 08-02-2019	MR.K.RAVITEJ VARMA ,technical manager, mr.k.rakesh kumar, technical manager enthu technology solutions india PVT.LTD, Coimbatore	93
3	CUSTOMS IC DESIGN USING MENTER EDA TOOLS	22-03-2019 To 23-03-2019	B.Nagendra, CoreEL Technologies, Hyderabad	94
4	Workshop on Methodologies on Embedded System Design	08-07-2018	Ajay Kumar Bolloju, Mr.k.prakash Jr. Embedded Developer at Trakartech Pvt. Ltd Hyderabad.	90
5	MATLAB Applications-signal and image processing tools	03-02-2016 To 04-02-2016	MR.K.RAVITEJ VARMA,technical manager, mr.k.rakesh kumar,technical manager enthu technology solutions india PVT.LTD, Coimbatore	114

## 2017-2018

S. No.	Gap	Date-month-Year	Resource Person with Designation	NO OF Students
1	Field Programmable Gate Array implementation using Verilog	15-02-2018	Mr.Sk.Chand Basha, Design Verification engineer, SION semiconductor Pvt. Ltd, Bangalore	200
2	Internet of Things with emphasis on sensors & interfacing	25-09-2017 To 26-09-2017	Mr.K.S.Rathina Swamy, Technical Manager, Enthu Technology Solutions India Pvt.Ltd, Coimbatore	99
3	Workshop on Methodologies on Embedded System Design	16-09-2017 To 17-09-2017	Ajay Kumar Bolloju Jr. Embedded Developer at Trakartech Pvt. Ltd Hyderabad.	94
4	RASPBERRY PI LINK ONE AND MICRO PYTHON	11-12-2017 To 13-12-2017	G.ASHOK,MR.A .GIREESH KUMAR , MR.K.RAMANA BABU ,DESIGN ENGINEER ALPHA SEMI CONDUCTORS PVT.LTD,BANGALORE	92

## 2016-17

S.No.	Gap	Date- Month-Year	Resource Person with designation	% of students
1	Software Testing Tools	15-09-2016 To 17-09-2016	Mr. Abdul Azeez, Test Manager, Capgemini, Hyderabad.	23
2	Workshop on circuit implementation using PCB Design	28-08-2016 To 29-08-2016	Nijil P,Senior instructor at SecNet,Chennai	92
3	Methodologies On Embedded system Design	02-07-2017	Mr.V.Govinda Rao, Mr.Y. Vijay Microlink Peripheral control Pvt. Ltd, Vijayawada	90
4	LATEST TRENDS IN VLSI	06-12-2016 TO 07-12-2016	Mr.Sk.Chand Basha,MR.RAGUNATH Design Verification engineer, SION semiconductor Pvt. Ltd, Bangalore	92

## 2015-16

S.No.	Gap	Date- Month-Year	Resource Person with designation	% of students
1	PCB Design	14-09-2015 To 15-09-2015	Mr.V.Govinda Rao, Microlink Peripheral control Pvt. Ltd, Vijayawada	,95
2	Methodologies On Embedded system Design	17-08-2015	Mr.V.Govinda Rao, mr.k.sharath, Microlink Peripheral control Pvt. Ltd, Vijayawada	90
3	Workshop on ASIC design flow using Mentor graphics tool	5-10-15 To 07-10-2015	Mr. Ankur Sangal ( Sr. Application Engineer , CoreEL Technologies) B.Nagendra, CoreEL Technologies, Hyderabad	90
4	MATLAB Applications-signal and image processing tools	3-02-2016 To 4-02-2016	MR.K.RAVITEJ VARMA,technical manager, Mr.k.rakesh kumar,technical manager enthu technology solutions india PVT.LTD, Coimbatore	114